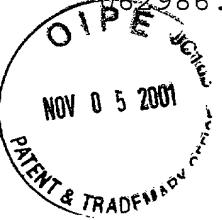


ATTORNEY DOCKET NO.
062986.0209

PATENT APPLICATION
09/910,630

1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Huffman, William A.
Serial No.: 09/910,630
Filing Date: July 20, 2001
Group Art Unit: Unknown
Title: METHOD AND SYSTEM FOR EFFICIENT USE
OF A MULTI-DIMENSIONAL SHARING VECTOR
IN A COMPUTER SYSTEM

Assistant Commissioner for
Patents
Washington, D.C. 20231

Dear Sir:

PRELIMINARY AMENDMENT

Prior to the initial review, please amend the Application as follows:

IN THE SPECIFICATION

Page 1, line 1, please insert the following paragraph:

--RELATED APPLICATIONS

This application claims the benefit of U.S. Provisional Application Serial No. 60/219,944 filed July 20, 2000. --

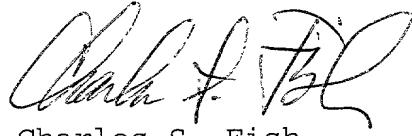
REMARKS

Attached herewith is a separate sheet showing a marked up version of the amendments made to the specification.

Attached herewith for the Examiner's and Draftsperson's approval are formal drawings for this application.

The Commissioner is hereby authorized to charge any amount required or credit any overpayment to Deposit Account No. 02-0384 of BAKER BOTTS L.L.P.

Respectfully submitted,
BAKER BOTTS L.L.P.
Attorneys for Applicants



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Date: November 5, 2001

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(15-4-1104.00)

PATENT APPLICATION

1

METHOD AND SYSTEM FOR EFFICIENT USE OF A MULTI-
DIMENSIONAL SHARING VECTOR
IN A COMPUTER SYSTEM

RELATED APPLICATIONS

This application claims the benefit of U.S.
Provisional Application Serial No. 60/219,944 filed July
20, 2000.

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TECHNICAL FIELD OF THE INVENTION

The present invention relates in general to memory access control in microprocessors, and more particularly to a directory structure for efficiently maintaining cache coherency.

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TO 5017-112907660

